

Feature Overview XC2000 Superset Devices

FEB 2011



	H-Series	M-Series	-Series	N-Series
Core	C166SV2	C166SV2	C166SV2	C166SV2
Core				
Frequency	100	80	80	80
Flash				
Program Flash	1536	768	704	256
Data Flash	64	64	64	64
SRAM				
Σ SRAM	138	50	82	34
Program [PS RAM]	112	32	64	16
Data Mem [DS RAM]	24	16	16	16
Dual Port [DPRAM]	2	2	2	2
Trace Mem for MCDS	4			
SB RAM				
Standby	8	8	1	8
CAN				
Channels	6	6	5	6
ADC				
Channels	24/30	16 / 24	16 / 24	9 / 16
Universal Serial Interface [USIC channels]*	10	8	6	6
Capture Compare				
CCU 1	1	0	0	0
CCU 2	1	1	1	1
Units				
CCU 6	4	4	4	2
[CCU]**				
FlexRay				
	2	-	-	-
Package				
	QFP 144 / 176	QFP 64 / 100 / 144	QFP 100 / 144	QFP 64 / 100
Temperatur (T_{ambient})				
	- 40°C to +125°C	- 40°C to +105/125°C	- 40°C to +125°C	- 40°C to +125°C

Superset	Series
SAK-XC2080H-200F100L AB	H
SAK-XC2060M-104F80L AA	M
SAK-XC2080M-104F80L AA	M
SAH-XC2030M-104F80L AA	M
SAK-XC2080-96F80L AC	-
SAK-XC2060-96F80L AC	-
SAK-XC2060N-40F80L AA	N
SAK-XC2030N-40F80L AA	N

* USIC: can be configured as UART, LIN, SPI/QSPI, IIC, IIS

** CCU: used for PWM, D/A

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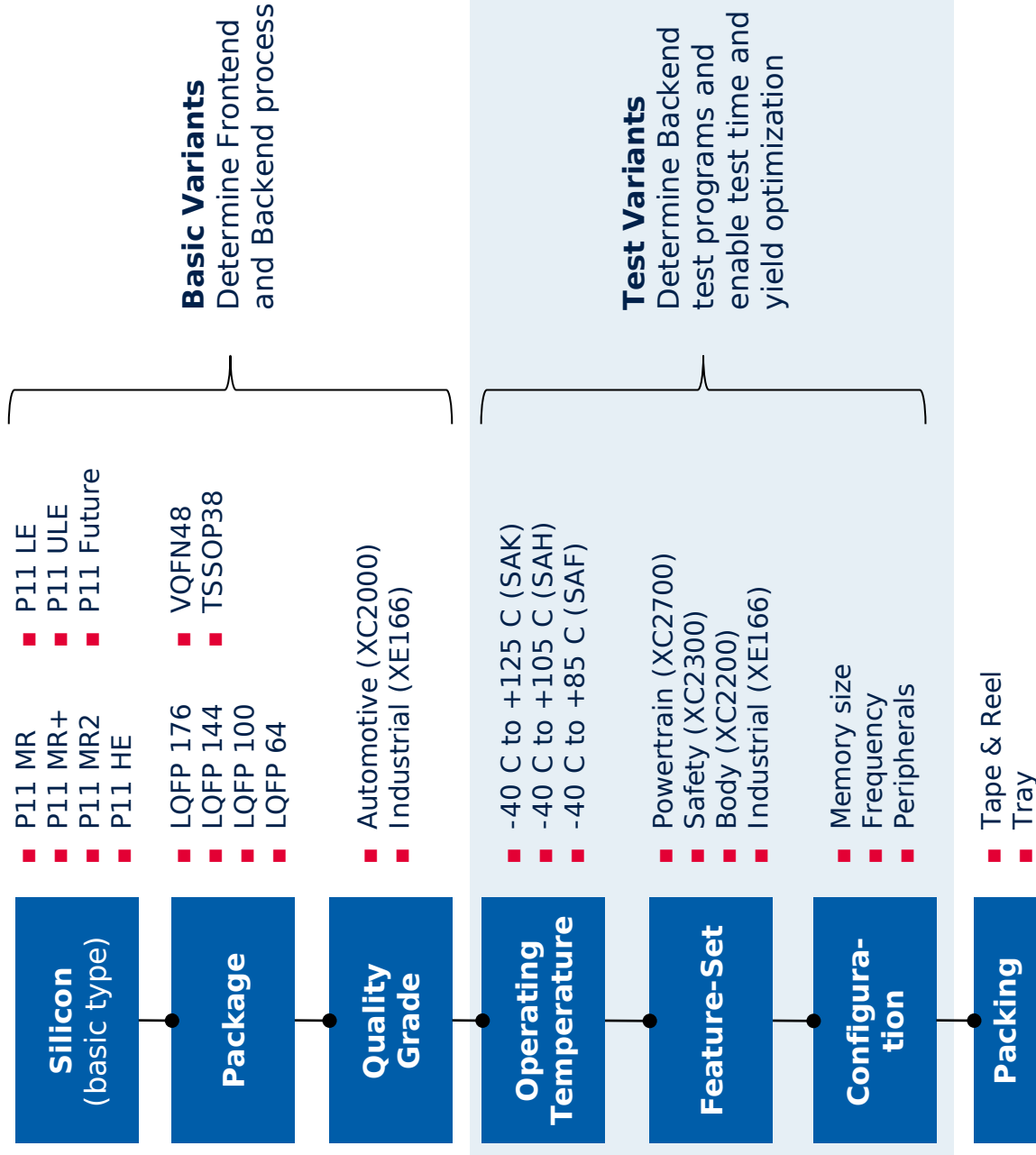


XC2000/XE166 Superset Delivery During Allocation Information Package

February 18th, 2011



XC2000/XE166 Variant Diversity



Temporary Variant Reduction



- The delivery situation at Infineon stays tight in 2011 in spite of a significant and continuing capacity increase
- Infineon needs to reduce the amount of variants to further increase the production throughput and optimize the delivery flexibility
- In CW 20 Infineon will start to deliver superset devices
- Exact starting date depends on the stock level of the original devices. Existing stock needs to be consumed first.
- Infineon will provide small quantities of superset device samples in CW18 to enable special clearance and to support a smooth phase-over
- This measure is planned until CW 48. Infineon will review the capacity situation in September and timely communicate an extension of this measure if necessary.



Superset Device Overview



Quality Grade	Superset	Basic Type	Tape & Reel	Tray
Automotive	SAK-XC2060M-104F80L AA	MR+ / 100 pin	Yes	Yes
	SAK-XC2080M-104F80L AA	MR+ / 144 pin	Yes	No
	SAK-XC2060N-40F80L AA	MR2 / 100 pin	Yes	No
	SAK-XC2030N-40F80L AA	MR2 / 64 pin	Yes	No
	SAK-XC2080-96F80L AC	MR / 144 pin	Yes	No
	SAK-XC2060-96F80L AC	MR / 100 pin	Yes	No
	SAH-XC2030M-104F80L AA	MR+ / 64 pin	Yes	No
	SAK-XC2080H-200F100L AB	HE / 144 pin	Yes	No
Industrial	SAK-XE164FM-72F80L AA	MR+ / 100 pin	Yes	No
	SAK-XE167FM-72F80L AA	MR+ / 144 pin	Yes	No
	SAK-XE164FN-40F80L AA	MR2 / 100 pin	Yes	Yes
	SAK-XE162FN-40F80L AA	MR2 / 64 pin	Yes	Yes
	SAF-XE167F-96F80L AC	MR / 144 pin	Yes	Yes
	SAF-XE164F-96F80L AC	MR / 100 pin	Yes	Yes
	SAF-XE162FM-72F80L AA	MR+ / 64 pin	Yes	No
	SAF-XE167FH-200F100L AB	HE / 144 pin	Yes	No

Superset Device Compatibility



- Superset devices are based on the same silicon in the same package as the respective device variants
- Superset devices are tested for maximum memory, frequency, feature and operating temperature requirements
- Superset devices are produced on the same Frontend and Backend process as the respective device variants
- **Superset devices will have the same electrical behavior and quality as the original device variants**
- Only deviation: different marking

*All affected customers will receive fully compatible
and in many cases higher graded devices*

- Customers need to order their original requested devices as today. All accepted quotes and agreed prices remain valid.
- The mapping of orders for specific variants to the respective superset devices is handled by Infineon. Full traceability is ensured.
- The superset devices will be delivered as “Substitute Products” for the originally ordered devices. This will be stated on the shipping notes.
- The shipping of superset devices will begin in CW20 but not before an existing stock of the original part has been consumed.

Boundary Conditions



- This is a mandatory measure to ensure and improve Infineon's supply capability during allocation
- Infineon is willing to change its cost optimized production process to an output optimized production process to improve the supply situation for its customers
- Infineon is aware that this measure will cause additional efforts at customers and OEMs to generate a special clearance for these substitutes
- Infineon will support customers best possible to ensure a smooth phase-over

Return to normal delivery mode



- Objective is to return to a normal delivery mode in CW 48 (beginning of December) and switch back to the originally ordered parts
- Infineon will review the capacity and demand situation in September 2011
- Infineon will timely inform customers latest by end of September in case an extension of this measure is necessary